

BOOT CONFIGURATIONS



TCXO

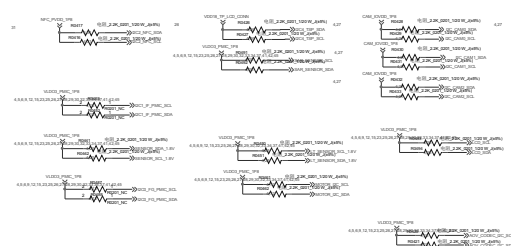


HW Board ID



R0411-R0414 need 1% accuracy

Period	Small outgroups	Large outgroups	Wilcoxon Rank-sum	Minimum	Max
20-00	0	842	8		
20-09-0.2	0	129	5		
18-18-0.29	0.1	183	20		
15-10-0.4	0.36	178	31		
04-02-0.53	0.69	173	43		
10-14-0.65	0.6	180	53		
10-06-0.77	0.12	90	63		
20-78-0.89	0.59	82	73		
20-50-1.01	0.36	75	79		
20-62-1.13	1.38	60	93		
13-14-1.25	1.2	50	103		
15-20-1.37	1.52	40	115		
13-17-1.49	1.94	30	127		
10-70-1.61	1.56	27	133		
10-62-1.73	1.58	22	143		
10-19-1.89	1.8	8	153		

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Title		<Title>
Author(s)		<Author(s)>
Date		<Date>

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MIPI

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Ver	1.00	Rev	1.00

MSM8953
RF

File		<Title>
File	Document Number	MSM8953
ID	<Doc>	<Rev C>
Rev	Revision 26 2010	Rev A of 44

V_SYSTEM

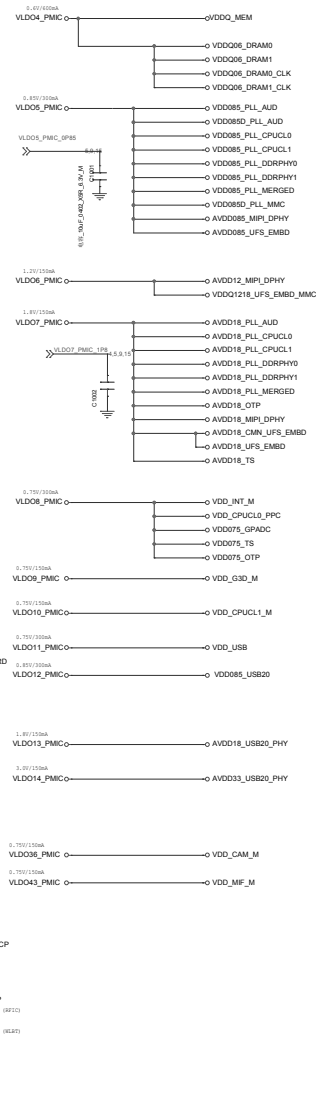
V_SYS

V_SYS_RF

NOTE: V_SYS and V_SYS_RF are connected to the same point as the V_SYSTEM pin.

Figure 10 is a schematic diagram illustrating the output voltage regulation for various Buck converters. The diagram shows the following connections and specifications:

- Buck1_PMC**: 0.75V/1500mA, connected to **VDD_MIF**.
- Buck2_PMC**: 0.75V/750mA, connected to **VDD_CPUCL1**.
- Buck3_PMC**: 0.75V/1800mA, connected to **VDD_CPUCL0**.
- Buck4_PMC**: 0.75V/2000mA, connected to **vDND_INT**.
- Buck5_PMC**: 0.75V/4000mA, connected to **vDND_GSD**.
- Buck6_PMC**: 0.75V/4000mA, connected to **vDND_CAM**.
- Buck7_PMC**: 0.75V/4000mA, connected to **vDND_VPX**.
- Buck8_PMC**: 1.1V/1500mA, connected to **vDND_MEM**.
- Buck9_PMC**: 1.1V/1500mA, connected to **vDND1_DRAM0_CKE** and **vDND1_DRAM1_CKE**.
- Buck10_PMC**: 2.0V/2000mA, connected to **vDND0_PMC1_LDO**.
- Buck11_PMC**: 0.75V/3000mA, connected to **vDND0_PMC1_LDO**.
- Buck12_PMC**: 0.75V/3000mA, connected to **vDND_CPB**.
- Buck13_PMC**: 0.75V/3000mA, connected to **vDND_WBG**.



1.8V/1.550A

VLD018_PMC1_O —

- AVDD08_DAO2_CP
- AVDD08_DAC1_CP
- AVDD08_ETDAC_CP
- AVDD08_WPLL_CP
- VDD08_BPLL_CP
- AVDD08_DAO2_WLBT
- AVDD08_DAC1_WLBT
- AVDD08_ADC_WLBT
- AVDD08_WPLL_WLBT
- AVDD08_ADC_GNSS
- AVDD08_GPLL_GNSS

1.8V/1.550A

VLD018_PMC1_O —

- AVDD18_ADC_CP
- AVDD18_DAO2_CP
- AVDD18_DAC1_CP
- AVDD18_ETDAC_CP
- AVDD18_WPLL_CP
- AVDD18_BPLL_CP
- AVDD18_ADC_WLBT
- AVDD18_DAO2_WLBT
- AVDD18_DAC1_WLBT
- AVDD18_WPLL_WLBT
- AVDD18_GPLL_GNSS

1.8V/1.550A

VLD012_PMC1_O —

- AVDD12_ADC_CP
- AVDD12_DAC1_CP
- AVDD12_ADC2_CP
- AVDD12_WPLL_CP
- AVDD12_ADC_WLBT
- AVDD12_WPLL_WLBT
- AVDD12_GPLL_GNSS

1.8V/2.85V/1.550A

VLD018_PMC1_O —

- VDD081830_USIM0_CP

1.8V/2.85V/1.550A

VLD018_PMC1_O —

- VDD081830_USIM1_CP

VLD033_FPMC	○	→	VDD_UFS_1P8
2.3V/1020mA			
VLD034_FPMC	○	→	VDD_UFS_2P95
3.3V/1020mA			
VLD035_FPMC	○	→	VDD_T_FLASH_3V0
3.3V/1020mA			
VLD037_FPMC	○	→	VDD_FPS_1P8
3.3V/1020mA			
VLD038_FPMC	○	→	REAR_CAM_A1VCC_2P8
3.3V/1020mA			
VLD039_FPMC	○	→	VDDI8_I_CD1
2.8V/120mA			
VLD040_FPMC	○	→	VDD_SENSOR_2P8
3.3V/120mA			
VLD041_FPMC	○	→	PM_CAM_FRT_AVDD_2P8
1.8V/1020mA			
VLD042_FPMC	○	→	VDD_CODEEC_1P8
1.3V/1020mA			
VLD044_FPMC	○	→	PM_CAM_REAR_DVDD_0

```

VLDQD02_PMC1 ○ ─────────── VDD18_TCXO
                                   │
                                   └── VRF18_GNSS (9F7C)

1.79713506s
VLDQD01_PMC1 ○ ─────────── D_VDD10_RF (8F6C)

1.79730306s
VLDQD02_PMC1 ○ ─────────── VRF_TRX_IP2 (8F6C)

1.79746506s
VLDQD03_PMC1 ○ ─────────── VRF12_TRX_LO (9F7C)

1.80170306s
VLDQD04_PMC1 ○ ─────────── VRF_TRX (8F6C)

2.80170306s
VLDQD05_PMC1 ○ ─────────── VRF 28

1.13111506s
VLDQD05_PMC1 ○ ─────────── VRF11_GNSS (9F7C)

1.13114506s
VLDQD07_PMC1 ○ ─────────── AVDD11_BTFM (9B21)
                                   │
                                   └── AVDD11_RF

1.79713506s
VLDQD08_PMC1 ○ ─────────── AVDD12_LO (8A20)

1.79713506s
VLDQD09_PMC1 ○ ─────────── AVDD18_ANA (8A20)

1.79713506s
VLDQD03_PMC1 ○ ─────────── AVDD18_DA (8A20)

1.79713506s
VLDQD01_PMC1 ○ ─────────── AVDD03_WL0 (8A20)

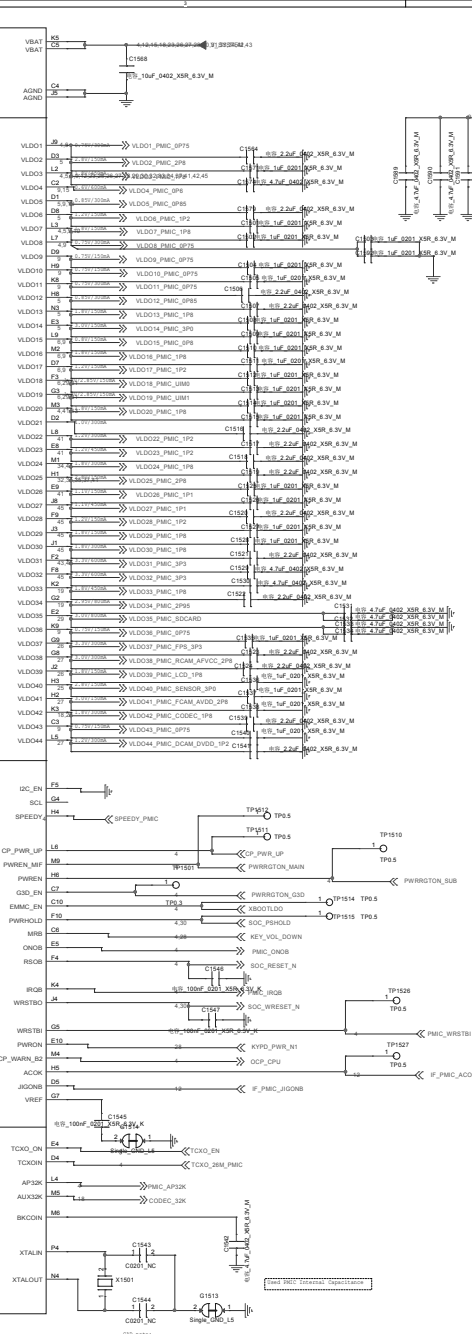
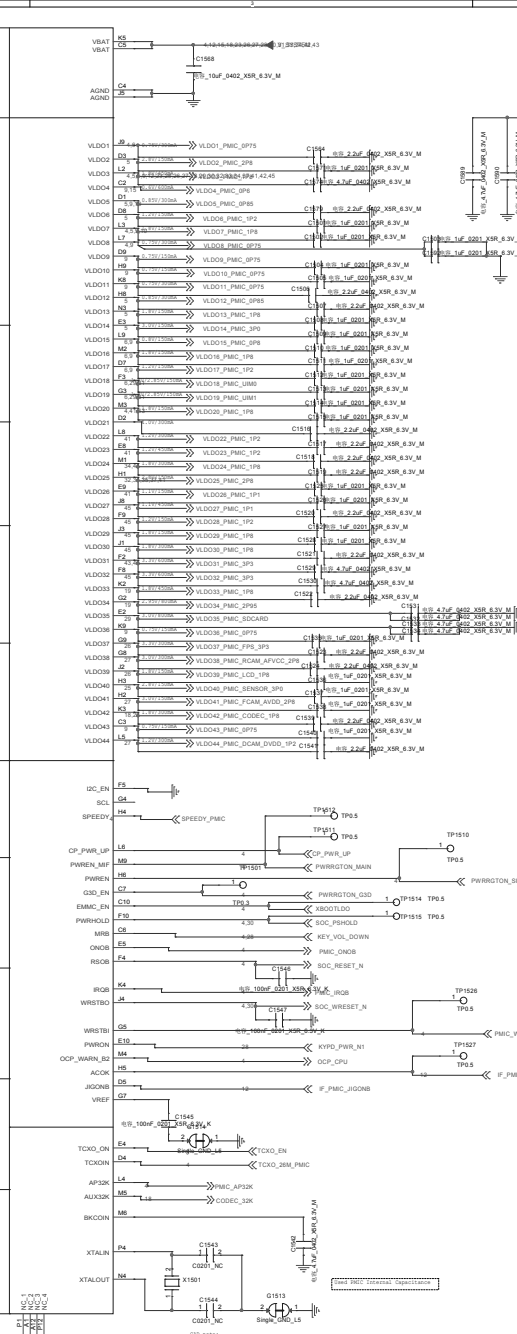
1.79713506s
VLDQD01_PMC1 ○ ─────────── AVDD03_WL1 (8A20)

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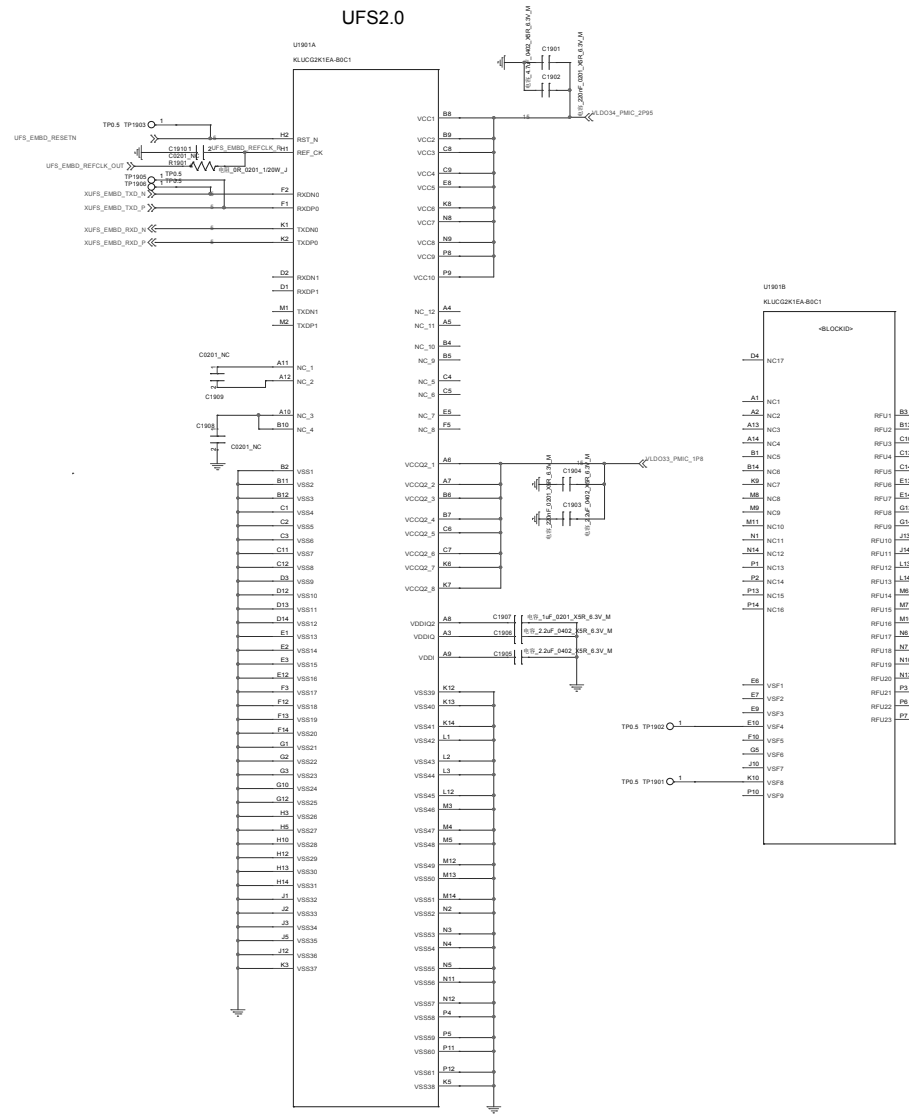
File			Name		
Size			Document Number		
D			Doc		
Date			Friday, January 25, 2019		
Sheet			10 of 44		

U0401E		
A1	VSS	B30
A2	VSS	B31
A31	VSS	B32
A32	VSS	B33
A33	VSS	B34
A34	VSS	B35
A35	VSS	B36
A36	VSS	B37
A37	VSS	B38
A38	VSS	B39
A39	VSS	B40
A40	VSS	B41
A41	VSS	B42
A42	VSS	B43
A43	VSS	B44
A44	VSS	B45
A45	VSS	B46
A46	VSS	B47
A47	VSS	B48
A48	VSS	B49
A49	VSS	B50
A50	VSS	B51
A51	VSS	B52
A52	VSS	B53
A53	VSS	B54
A54	VSS	B55
A55	VSS	B56
A56	VSS	B57
A57	VSS	B58
A58	VSS	B59
A59	VSS	B60
A60	VSS	B61
A61	VSS	B62
A62	VSS	B63
A63	VSS	B64
A64	VSS	B65
A65	VSS	B66
A66	VSS	B67
A67	VSS	B68
A68	VSS	B69
A69	VSS	B70
A70	VSS	B71
A71	VSS	B72
A72	VSS	B73
A73	VSS	B74
A74	VSS	B75
A75	VSS	B76
A76	VSS	B77
A77	VSS	B78
A78	VSS	B79
A79	VSS	B80
A80	VSS	B81
A81	VSS	B82
A82	VSS	B83
A83	VSS	B84
A84	VSS	B85
A85	VSS	B86
A86	VSS	B87
A87	VSS	B88
A88	VSS	B89
A89	VSS	B90
A90	VSS	B91
A91	VSS	B92
A92	VSS	B93
A93	VSS	B94
A94	VSS	B95
A95	VSS	B96
A96	VSS	B97
A97	VSS	B98
A98	VSS	B99
A99	VSS	B100
A100	VSS	B101
A101	VSS	B102
A102	VSS	B103
A103	VSS	B104
A104	VSS	B105
A105	VSS	B106
A106	VSS	B107
A107	VSS	B108
A108	VSS	B109
A109	VSS	B110
A110	VSS	B111
A111	VSS	B112
A112	VSS	B113
A113	VSS	B114
A114	VSS	B115
A115	VSS	B116
A116	VSS	B117
A117	VSS	B118
A118	VSS	B119
A119	VSS	B120
A120	VSS	B121
A121	VSS	B122
A122	VSS	B123
A123	VSS	B124
A124	VSS	B125
A125	VSS	B126
A126	VSS	B127
A127	VSS	B128
A128	VSS	B129
A129	VSS	B130
A130	VSS	B131
A131	VSS	B132
A132	VSS	B133
A133	VSS	B134
A134	VSS	B135
A135	VSS	B136
A136	VSS	B137
A137	VSS	B138
A138	VSS	B139
A139	VSS	B140
A140	VSS	B141
A141	VSS	B142
A142	VSS	B143
A143	VSS	B144
A144	VSS	B145
A145	VSS	B146
A146	VSS	B147
A147	VSS	B148
A148	VSS	B149
A149	VSS	B150
A150	VSS	B151
A151	VSS	B152
A152	VSS	B153
A153	VSS	B154
A154	VSS	B155
A155	VSS	B156
A156	VSS	B157
A157	VSS	B158
A158	VSS	B159
A159	VSS	B160
A160	VSS	B161
A161	VSS	B162
A162	VSS	B163
A163	VSS	B164
A164	VSS	B165
A165	VSS	B166
A166	VSS	B167
A167	VSS	B168
A168	VSS	B169
A169	VSS	B170
A170	VSS	B171
A171	VSS	B172
A172	VSS	B173
A173	VSS	B174
A174	VSS	B175
A175	VSS	B176
A176	VSS	B177
A177	VSS	B178
A178	VSS	B179
A179	VSS	B180
A180	VSS	B181
A181	VSS	B182
A182	VSS	B183
A183	VSS	B184
A184	VSS	B185
A185	VSS	B186
A186	VSS	B187
A187	VSS	B188
A188	VSS	B189
A189	VSS	B190
A190	VSS	B191
A191	VSS	B192
A192	VSS	B193
A193	VSS	B194
A194	VSS	B195
A195	VSS	B196
A196	VSS	B197
A197	VSS	B198
A198	VSS	B199
A199	VSS	B200

SSE009_T80

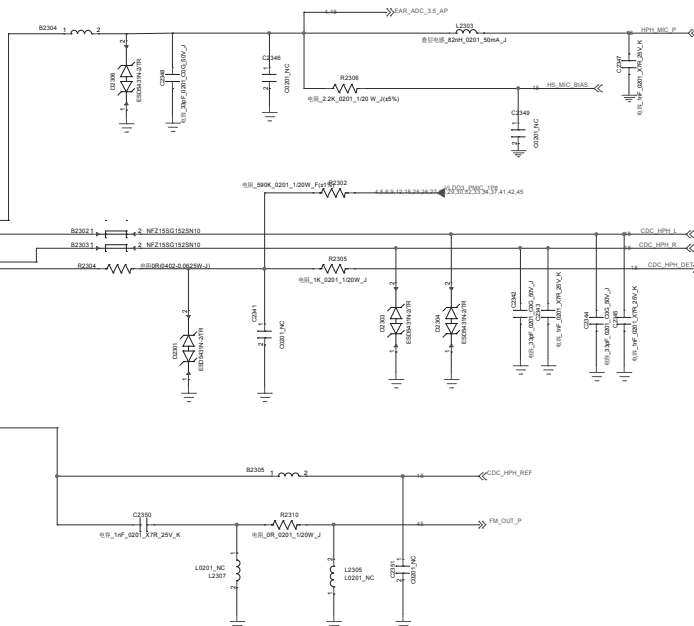
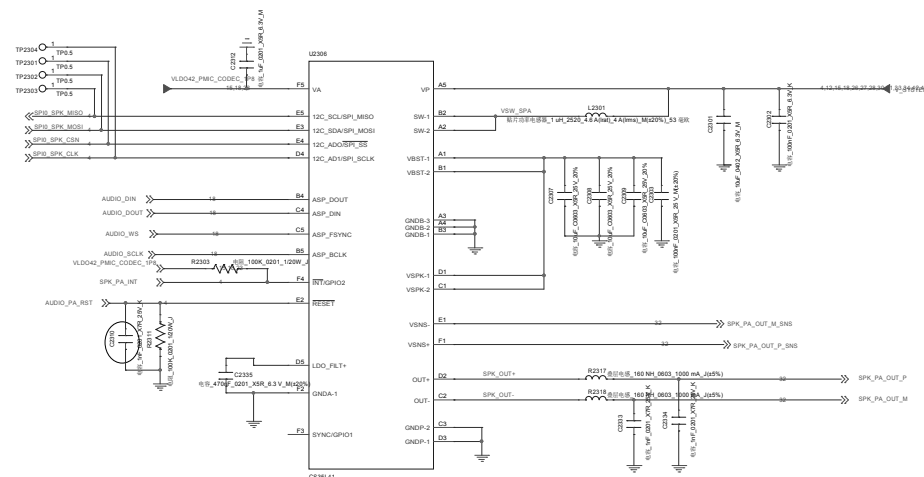


UFS2.0



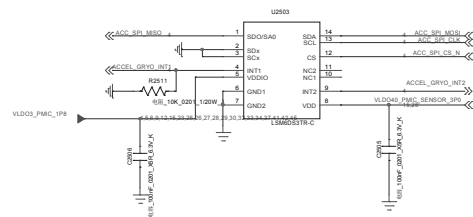
CAD Notes :
Place TP close to the via to minimize stub length.

CC Logic??

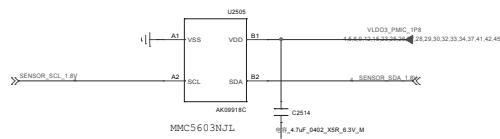
[illegible]

ACCELEROMETER+GYRO

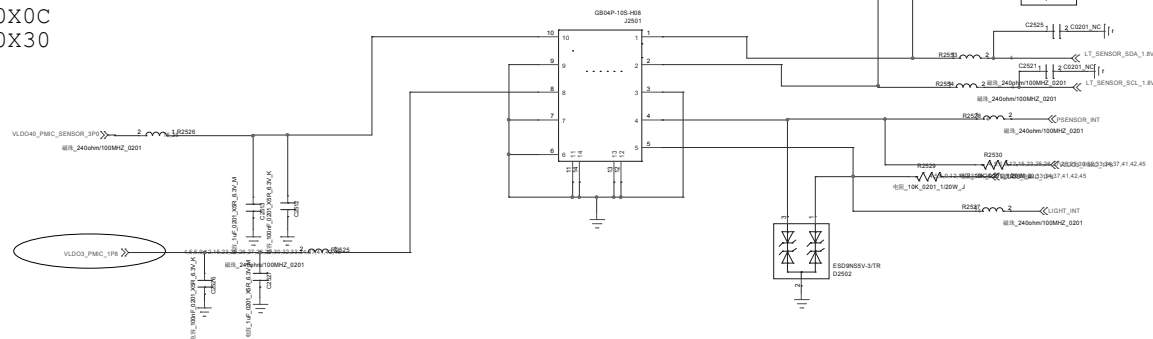
I2C ADDRESS 0X68 0b1101000



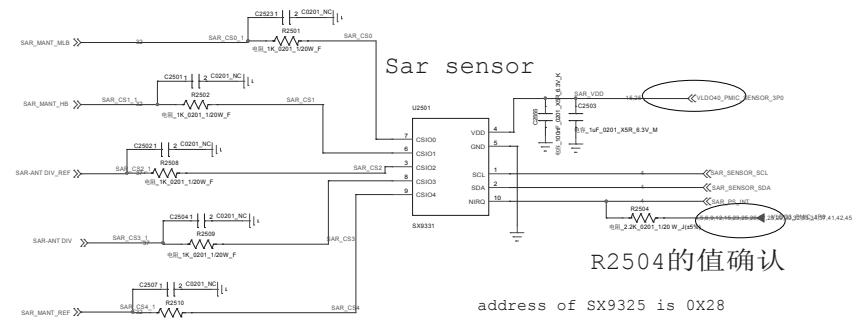
Magnetic Sensor



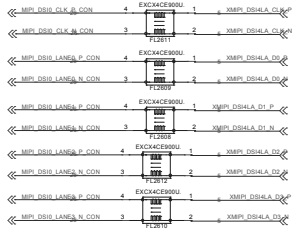
address of AK09918 is 0X0C
MMC5603NJL is 0X30



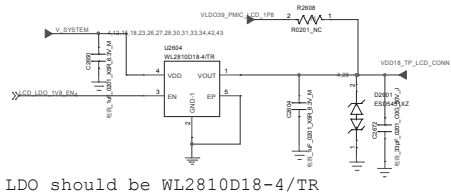
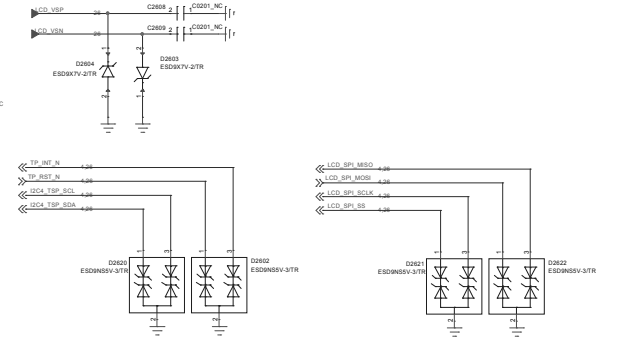
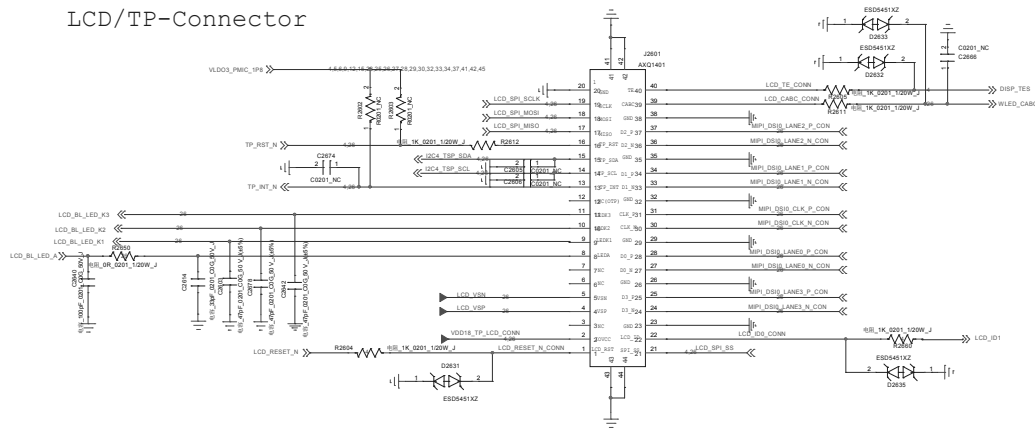
VLD040_PMIC_SENSOR_3P0 need Software change to 3.0V



LCD MIPI Common Mode Filter



LCD/TP-Connector

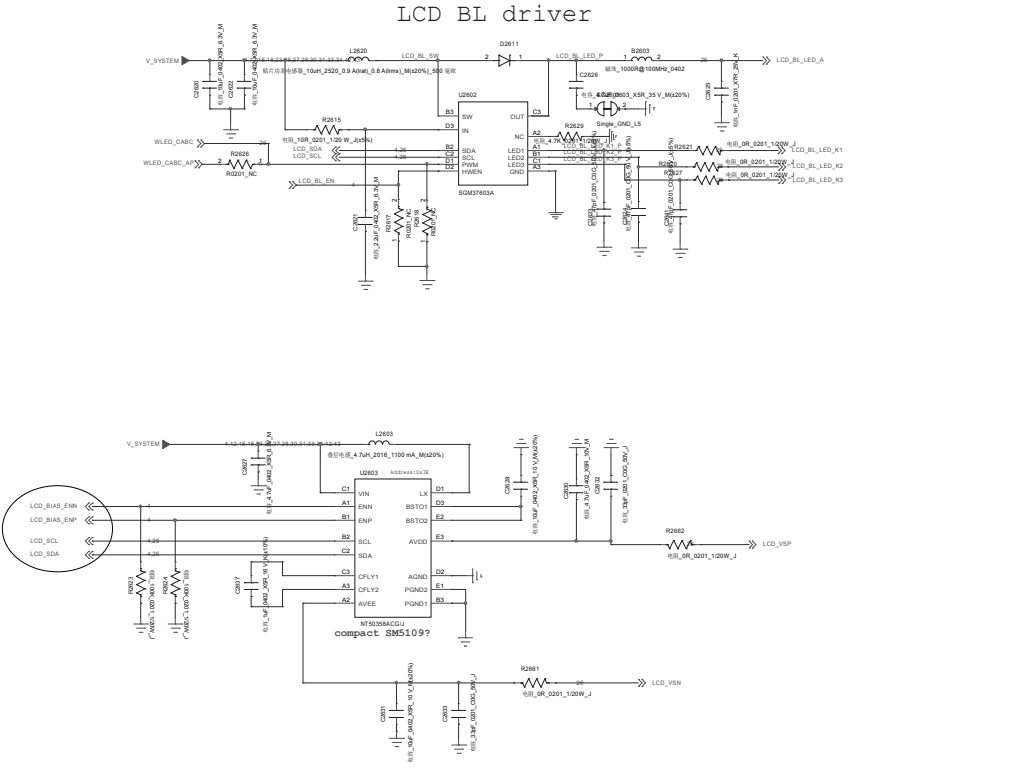
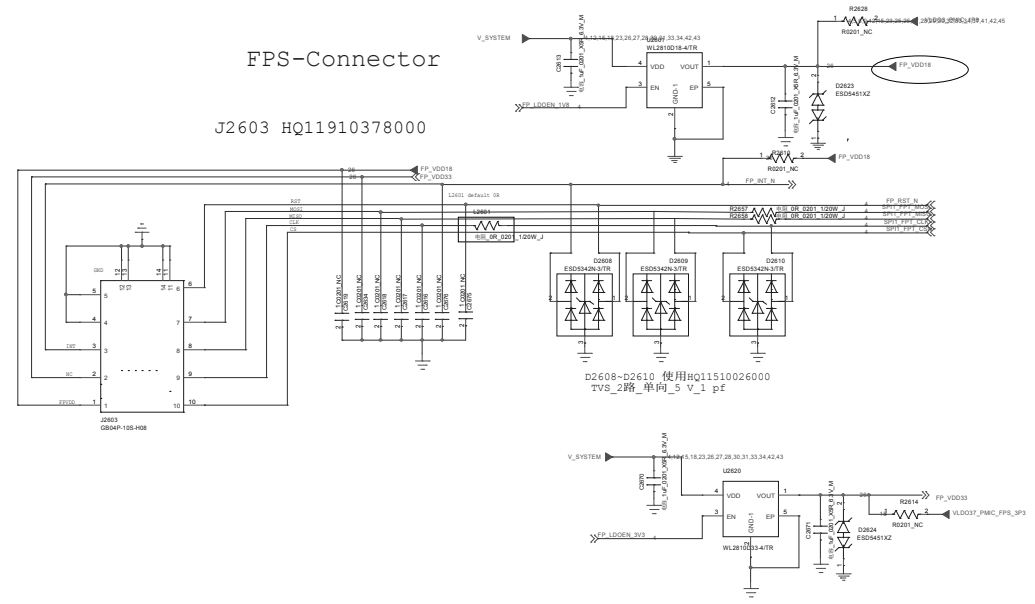


LDO should be WL2810D18-4/TR

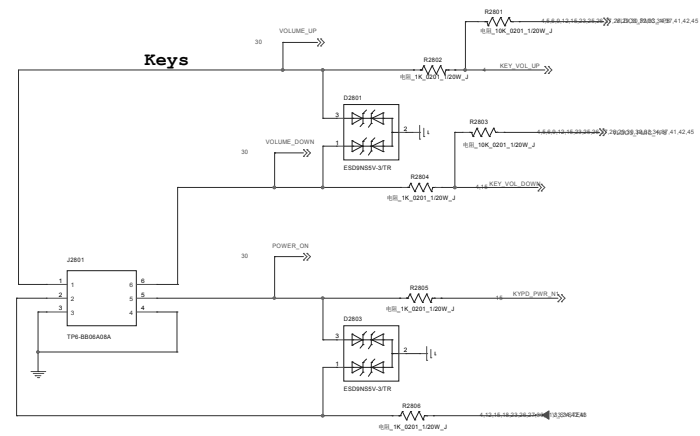
	IOVDD	
LCD	1.8V	EXT LD06/LD039
FPS	1.8V	EXT LD07/LD037

FPS-Connector

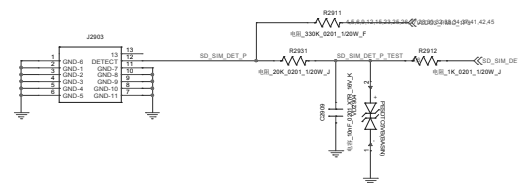
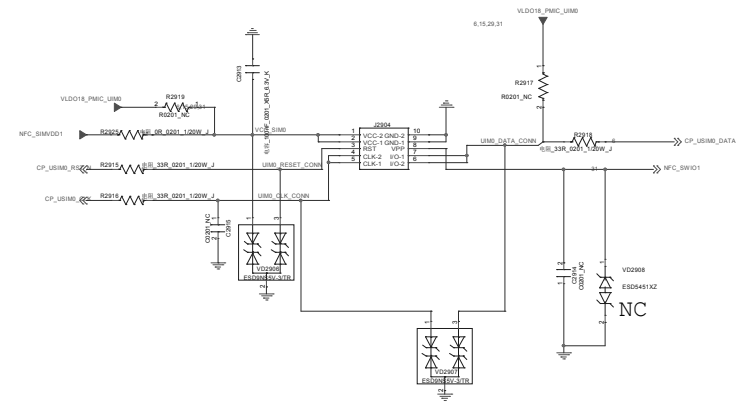
J2603 HQ11910378000



音量键&开关机键

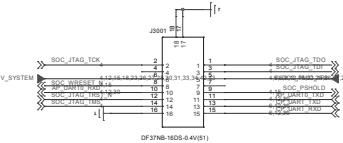


File <Title>			
Size D	Document Number <Doc>		Rev <Rev Code>
Date:	Friday, January 25, 2019	Sheet	28 of 44

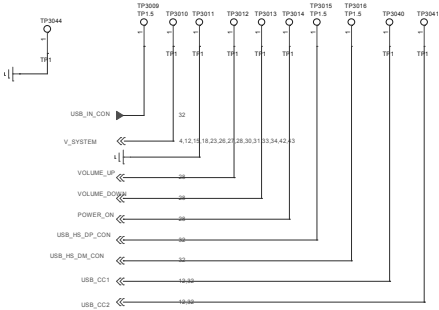
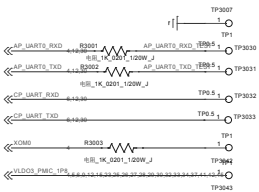


JTAG CONNECTOR

Lenovo SW debug request regarding moto

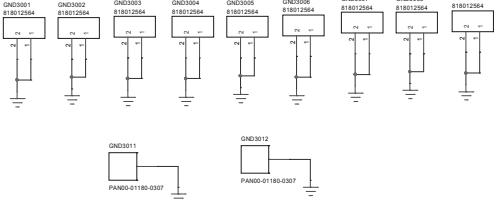
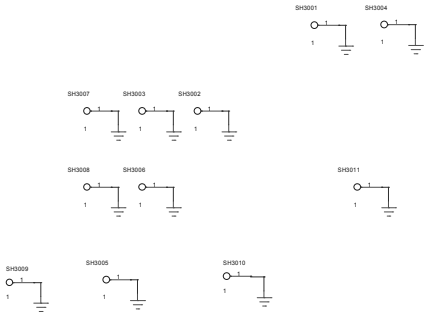


NO WATCH DOG

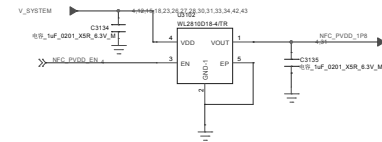
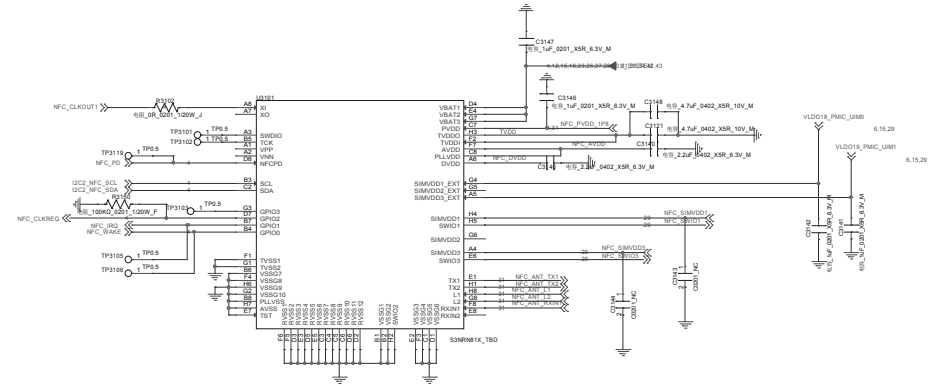
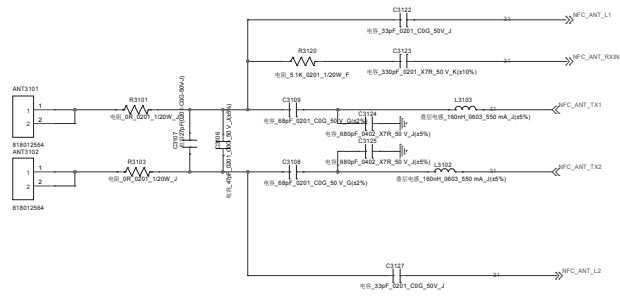


PCBA AUTOMATIC TEST point

接地弹片 TOP

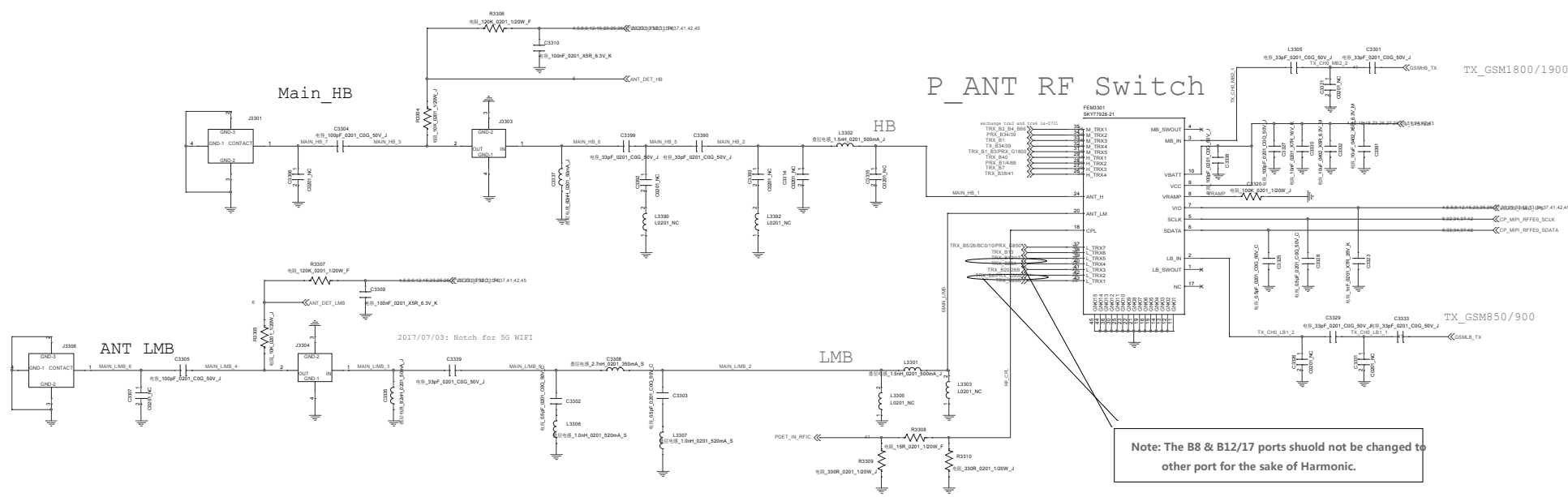


NFC

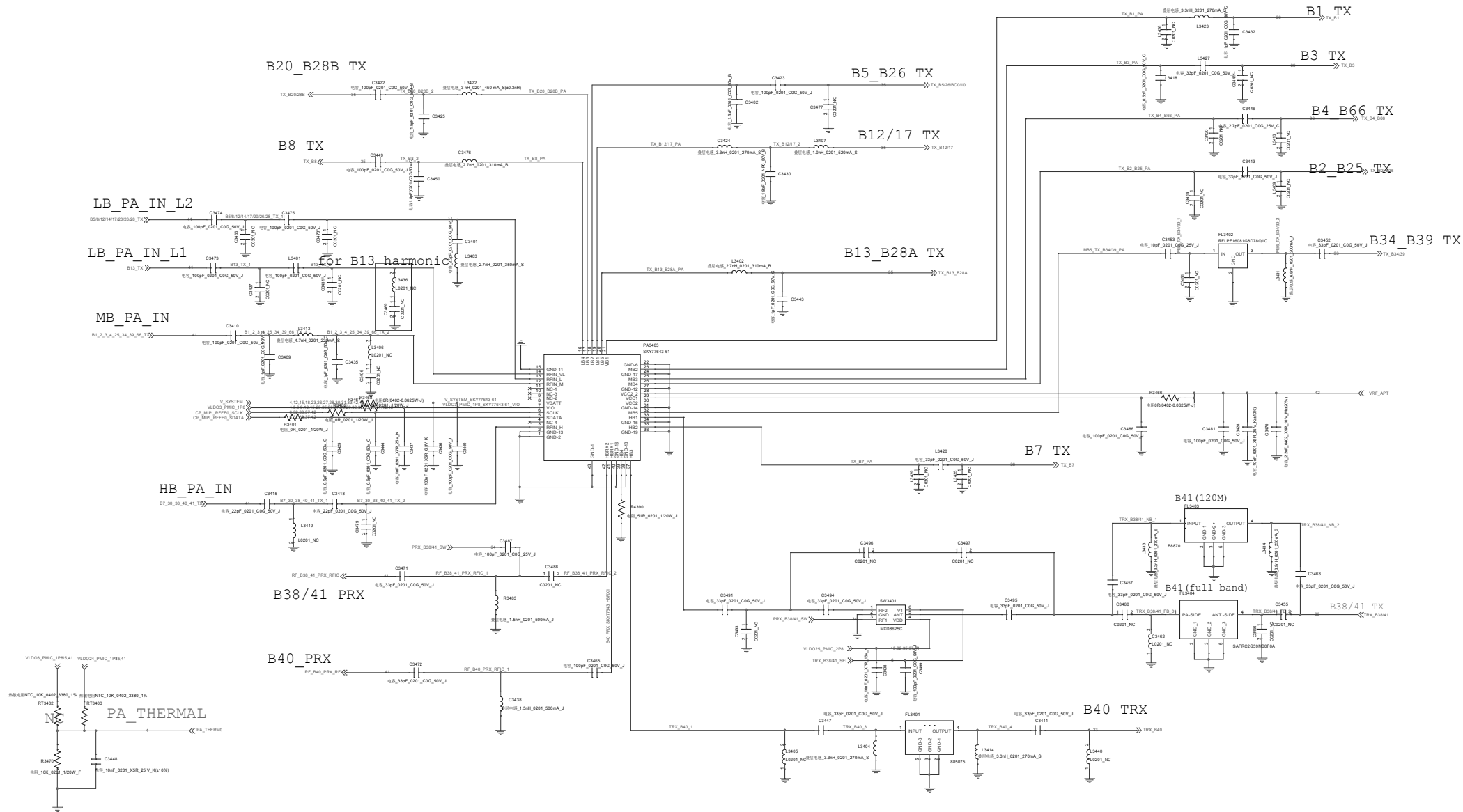


U3102: BOM中用1.8V的LDO

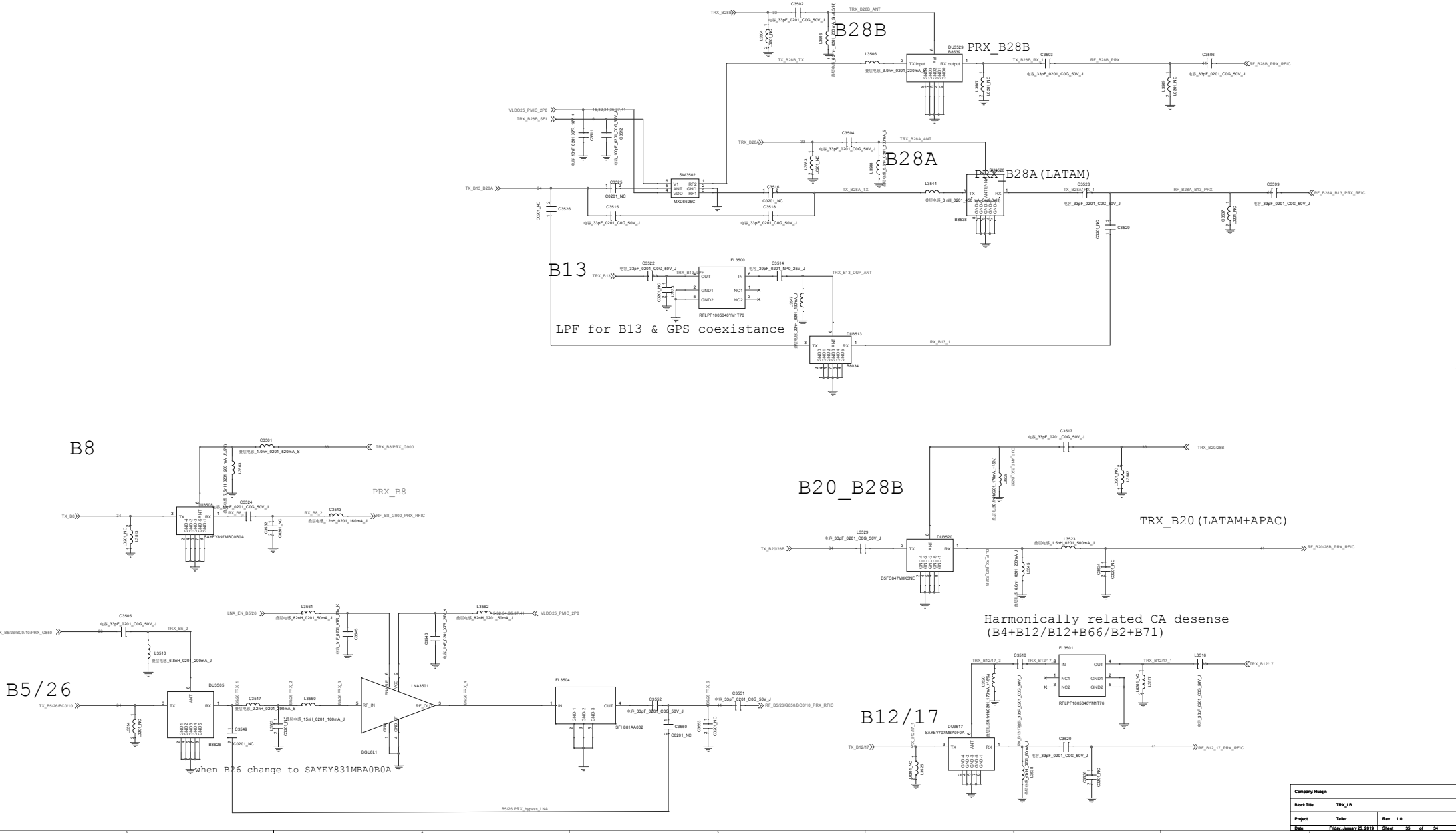
PRIMARY_ ANTENNA



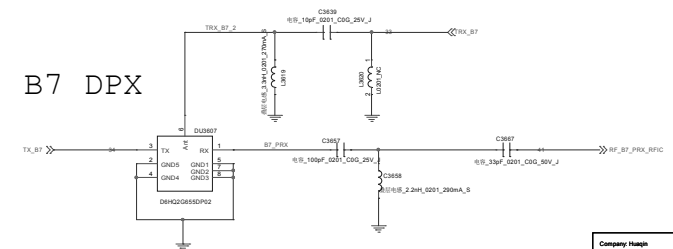
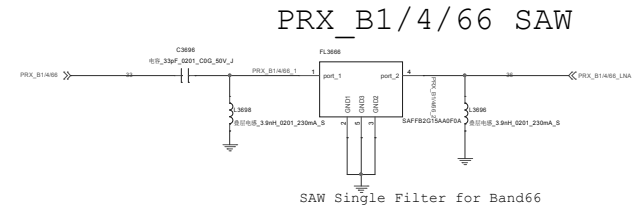
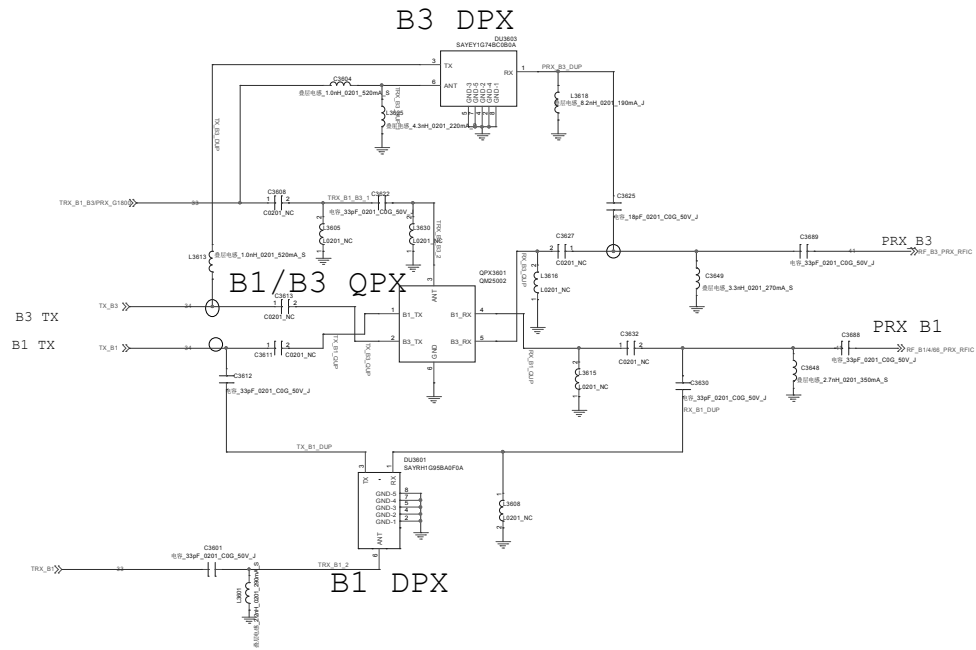
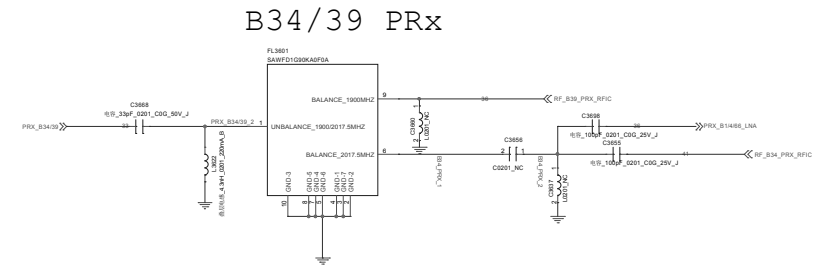
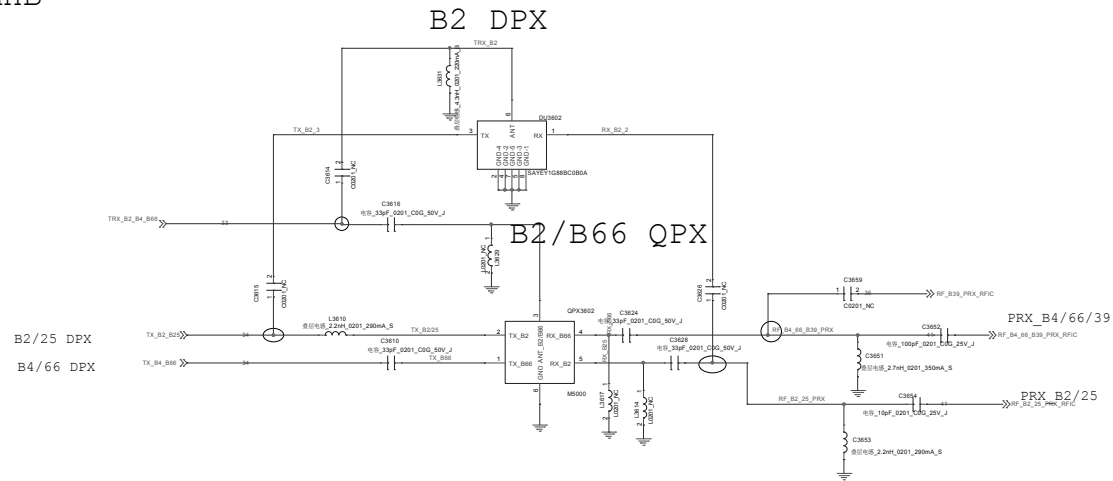
Company Hsien			
Stock Title	PRIMARY_ ANTENNA		
Project	Taller	Rev	1.0
Date	Follow January 26, 2019	Sheet	53 of 59



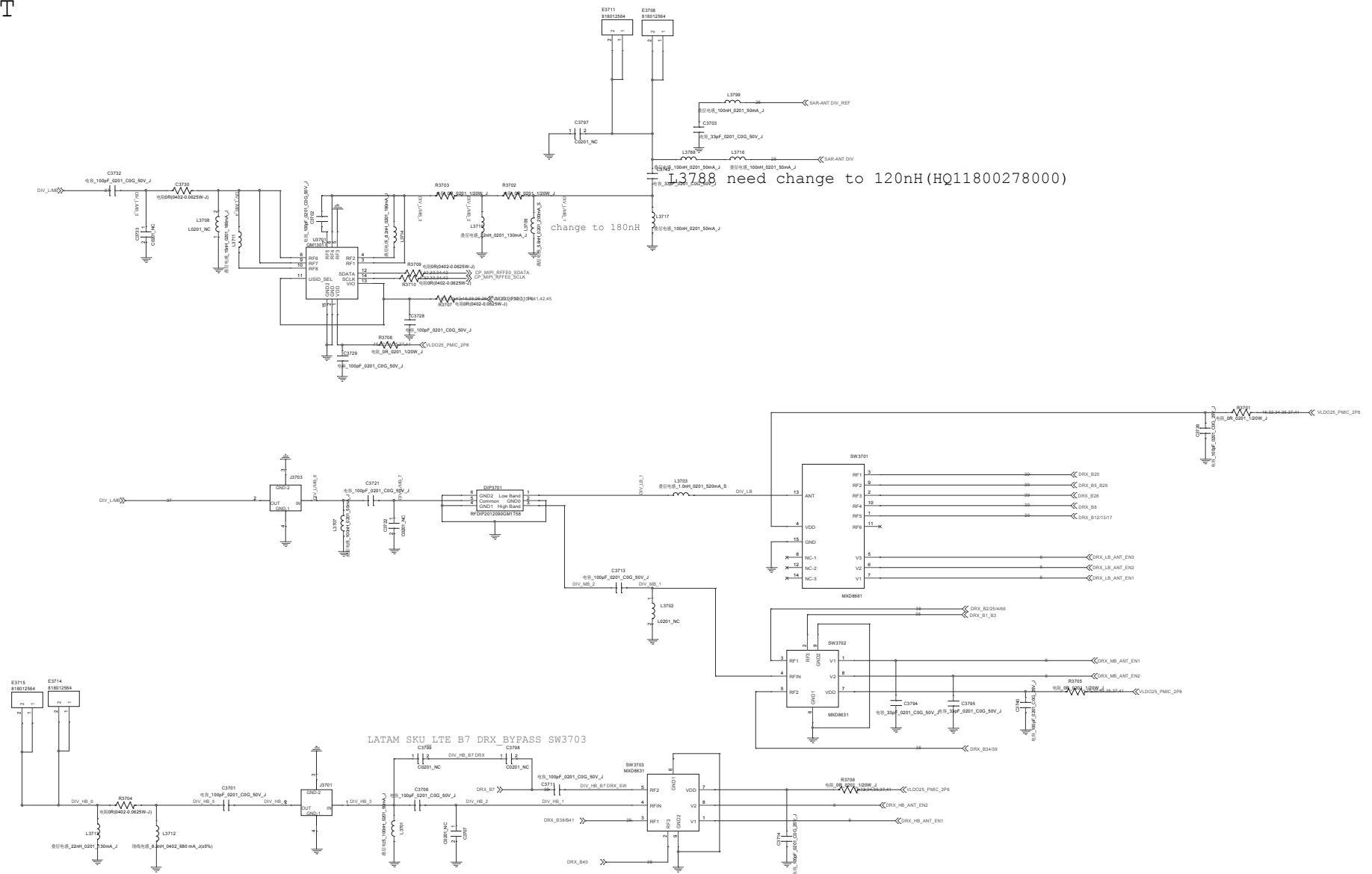
Company Hsien			
Block Title	PA & PA THERMAL		
Project	Taller	Rev	1.0
Date	Friday, January 24, 2020	Sheet	50 of 50



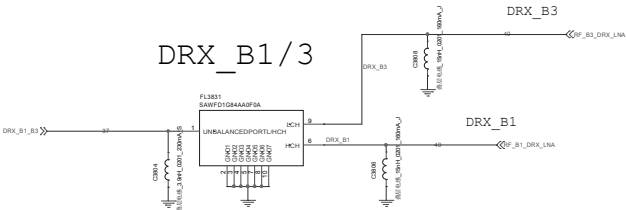
TRX_MHB



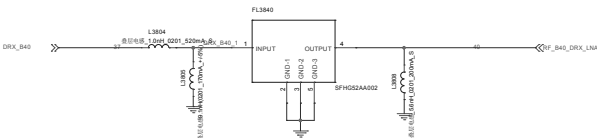
DIVERSIT_ ANT



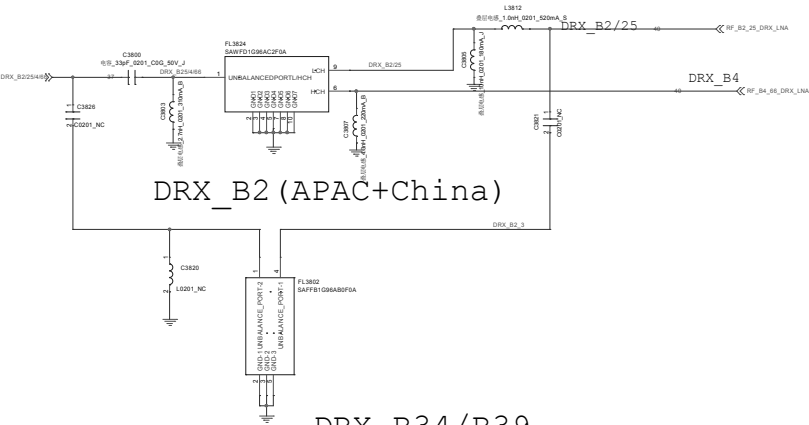
DRX_MB_HB



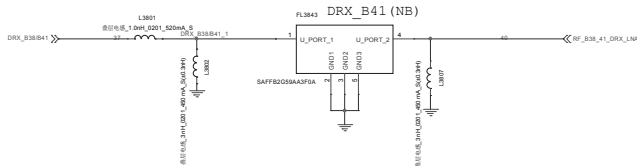
DRX_B40



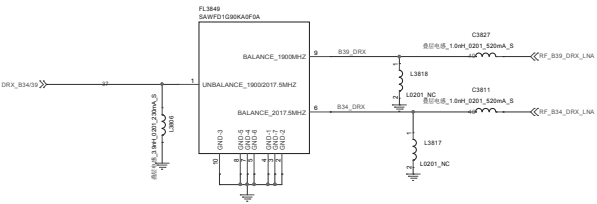
DRX_B2/25/4/66



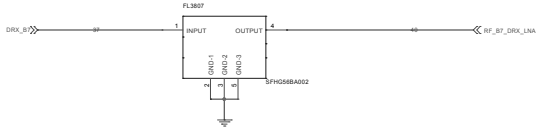
DRX_B38/41



DRX_B34/B39

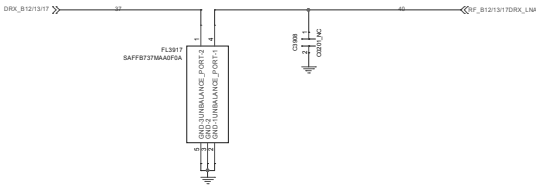


DRX_B7

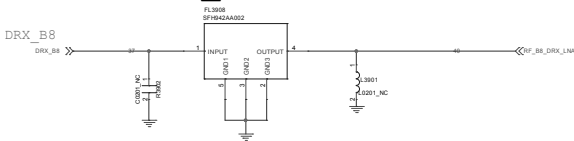


DRX_LB

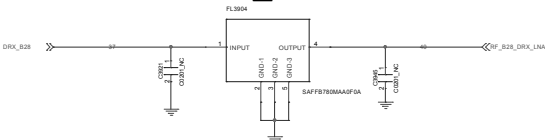
DRX_B12/13/17



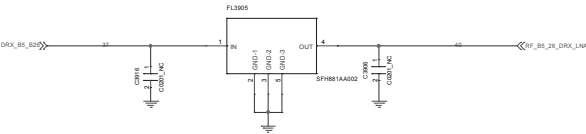
DRX_B8



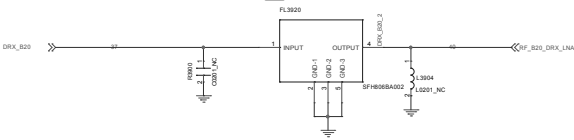
DRX_B28

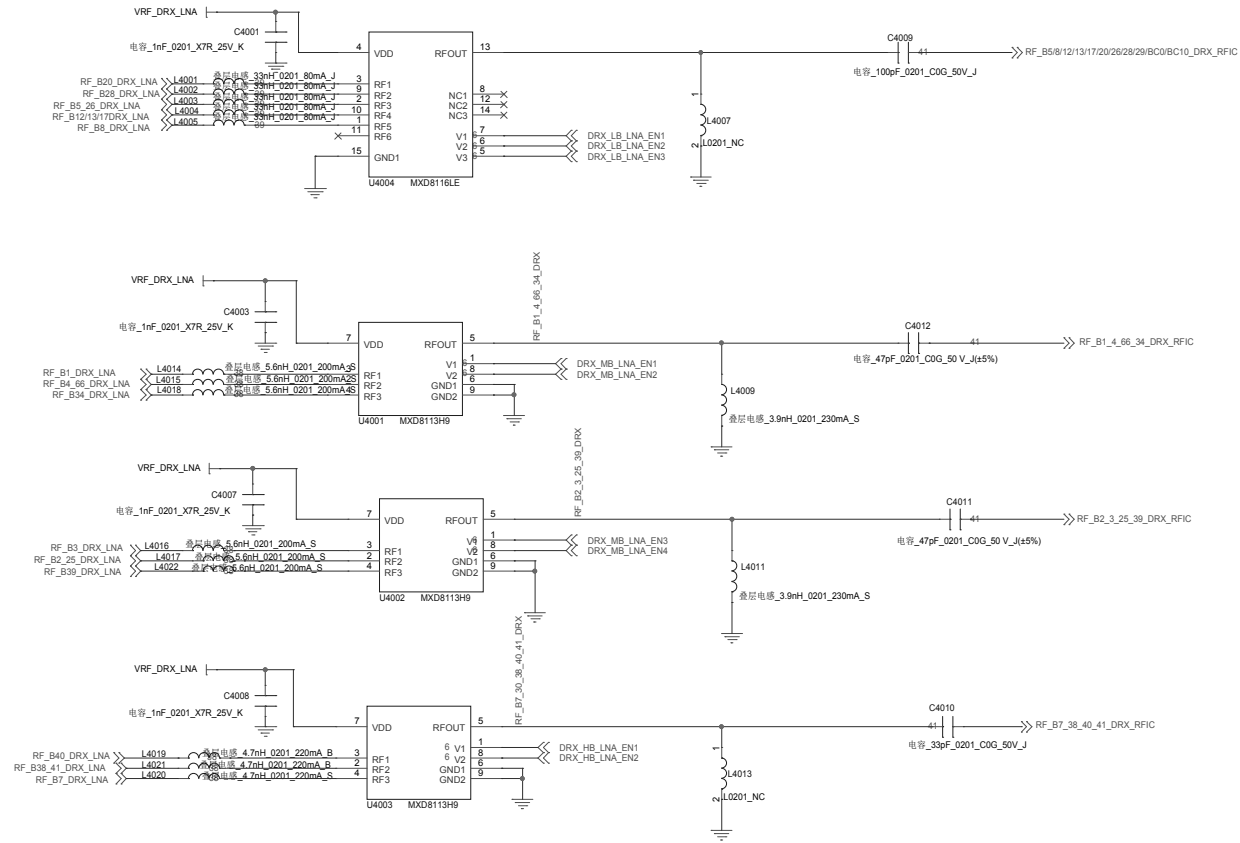


DRX_B5/26

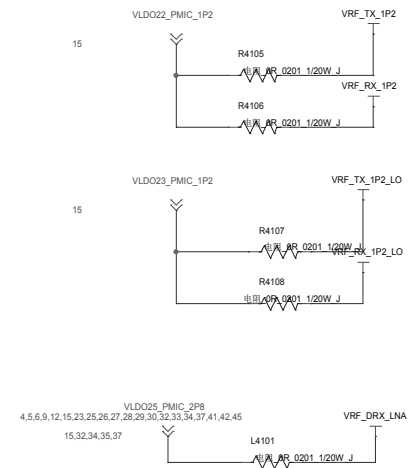
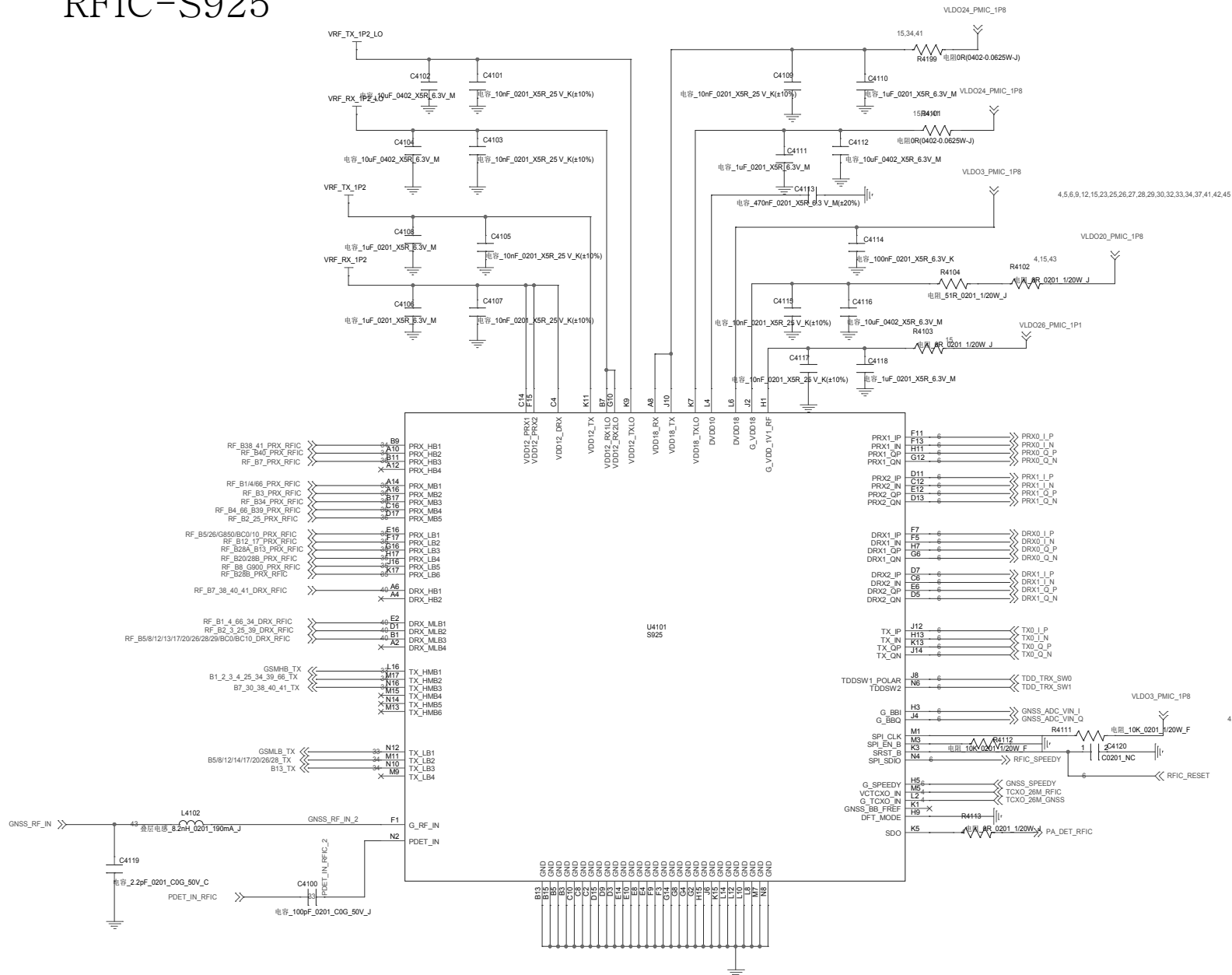


DRX_B20





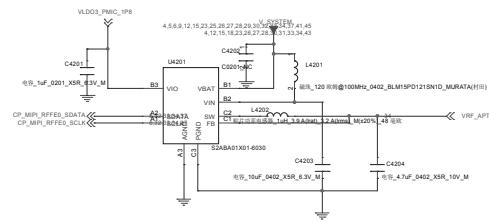
RFIC-S925



Title <Title>			
Size C	Document Number <Doc>		Rev <Rev Code>
Date: Friday, January 25, 2019	Sheet	41	of 40

S2ABA01X01-6030 APT

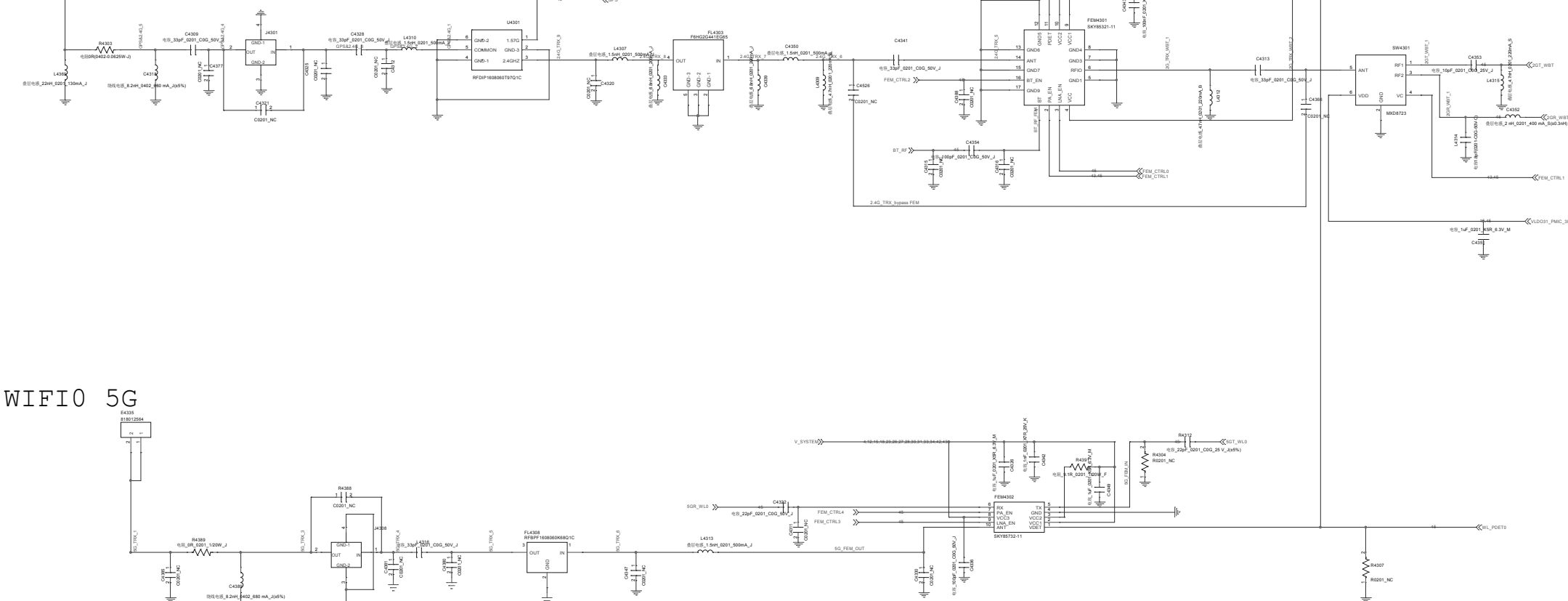
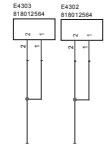
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Company Hsien			
Stock Title		QET4101	
Project	Taller	Rev	1.0
Date	2019-05-26	Sheet	41 of 41

WIFI0 2.4G/BT/GPS

WIFI0 5G



Company Name			
Stock Title	WPI 2450SLBT		
Project	Teller	Rev	1.0
Date	Friday, January 26, 2018	Sheet	43 of 43

